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~~U.S. PATENT DOCUMENTS~~

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## FOREIGN PATENT DOCUMENTS

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**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

W	A1	"A Set of Analytic Formulas for Capacitance of VLSI Interconnects of Trapezium Shape," by Ganesh Shankar Samudra and Hsio Lin Lee; IEEE Transactions on Electron Devices, Vol. 41, No. 8, August 1994; (4 pages).
W	A2	"Inductance and Capacitance Analytic Formulas for VLSI Interconnects," by N. Delorme, M. Belleville and J. Chilo; Electronics Letter, Vol. 32, No. 11, May 23, 1996; (4 pages).

**EXAMINER**

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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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